



## N- Channel Enhancement Mode MOSFET

**◆ DESCRIPTION**

The MT2562 is the N-Channel logic enhancement mode power field effect transistor is produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other Battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

**◆ FEATURES**

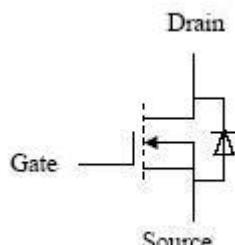
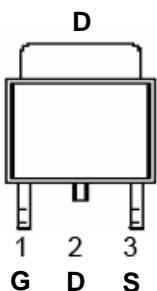
- 60V/15A,  $R_{DS(ON)} = 45m\Omega$  @  $V_{GS} = 10V$
- 60V/10A,  $R_{DS(ON)} = 60m\Omega$  @  $V_{GS} = 5V$
- Super high density cell design for extremely ultra low  $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- TO-252 package design

**◆ APPLICATIONS**

- POWER Management
- Portable Equipment
- DC/DC Converter
- Load Switch
- DSC

**◆ PIN CONFIGURATION**

TO-252(Top Site)



**N- Channel Enhancement Mode MOSFET**
**◆ ABSOLUTE MAXIMUM RATINGS**

( $T_A=25^\circ\text{C}$  Unless Otherwise Noted)

Parameter	Symbol	Maximum	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <small><math>T_A = 25^\circ\text{C}</math></small>	$I_D$	15	A
		12	
Pulsed Drain Current <sup>A</sup>	$I_{DM}$	40	A
Power Dissipation <small><math>T_A = 25^\circ\text{C}</math></small>	$P_D$	32	W
		22	
Operating junction temperature range	$T_J$	- 55 to 150	$^\circ\text{C}$
Storage temperature range	$T_{STG}$	- 55 to 150	$^\circ\text{C}$
Lead Temperature( 1/16" from case for 10 Sec.)	$T_L$	275	$^\circ\text{C}$

Note A: Pulse width limited by maximum junction temperature.

Note B: Duty Cycle  $\leq 1\%$

**◆ THERMAL RESISTANCE RATINGS**

Thermal Resistance	Symbol	Maximum	Unit
Junction-to-Case	$R_{\theta JC}$	3	$^\circ\text{C}/\text{W}$
Junction-to-Ambient	$R_{\theta JA}$	75	$^\circ\text{C}/\text{W}$

**◆ ORDERING INFORMATION**

Device	Package	Shipping
MT2562	TO-252	2,500 PCS / Tape & Reel



## N- Channel Enhancement Mode MOSFET

◆ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C Unless Otherwise Noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250µA	1	1.5	3	V
Gate Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ± 20V	-	-	±250	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V	-	-	1	µA
		V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125 °C	-	-	10	
On-State Drain Current <sup>C</sup>	I <sub>D(ON)</sub>	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 10V				
Drain-Source On Resistance <sup>C</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A	-	35	45	mΩ
		V <sub>GS</sub> = 5V, I <sub>D</sub> = 15A	-	42	60	
Forward Trans conductance <sup>C</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 15A	-	15	-	S
<b>Dynamic Parameters</b>						
Input Cap.	C <sub>iss</sub>	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz	-	790	-	pF
Output Cap.	C <sub>oss</sub>		-	98	-	
Reverse Transfer Cap.	C <sub>rss</sub>		-	42	-	
Total Gate Charge <sup>D</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 0.5V <sub>(BR)DSS</sub> , V <sub>GS</sub> = 10V, I <sub>D</sub> = 5A	-	15.2	22	nC
	Q <sub>gs</sub>		-	2.9	-	
	Q <sub>gd</sub>		-	3.2	-	
Turn-On Time <sup>D</sup>	T <sub>D(ON)</sub>	V <sub>DS</sub> = 30V, I <sub>D</sub> = 1A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = 6Ω	-	13.5	24	nS
	t <sub>r</sub>		-	9.8	18	
Turn-Off Time <sup>D</sup>	T <sub>D(OFF)</sub>		-	23	40	
	t <sub>f</sub>		-	7.2	16	
<b>Source-Drain Diode Ratings And Characteristics</b>						
Continuous Current	I <sub>S</sub>		-	-	15	A
Pulsed Current	I <sub>SM</sub>				40	A
Forward Voltage <sup>C</sup>	V <sub>SD</sub>	I <sub>F</sub> = I <sub>S</sub> , V <sub>GS</sub> = 0V	-	-	1.3	V

Note C: Pulse test: Pulse width ≤ 300µsec, Duty Cycle ≤ 2%

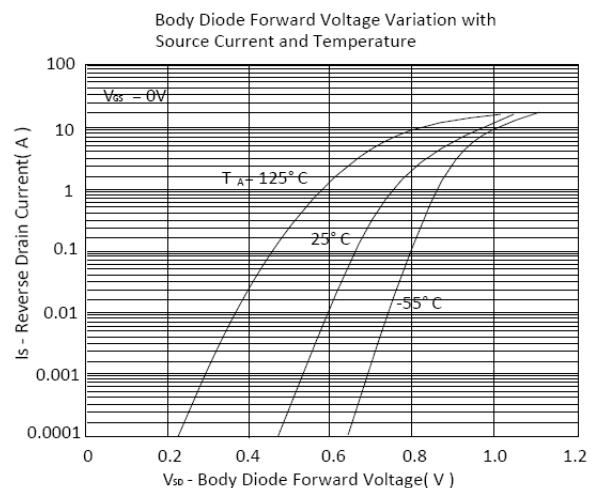
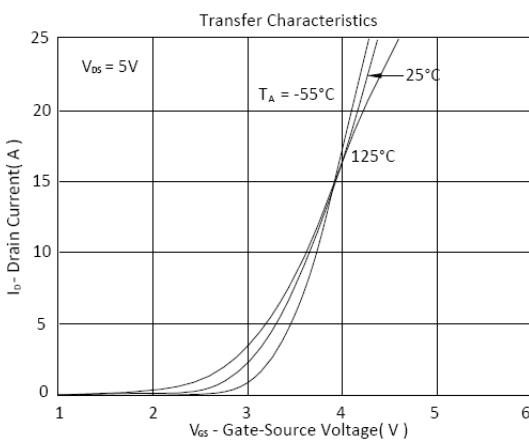
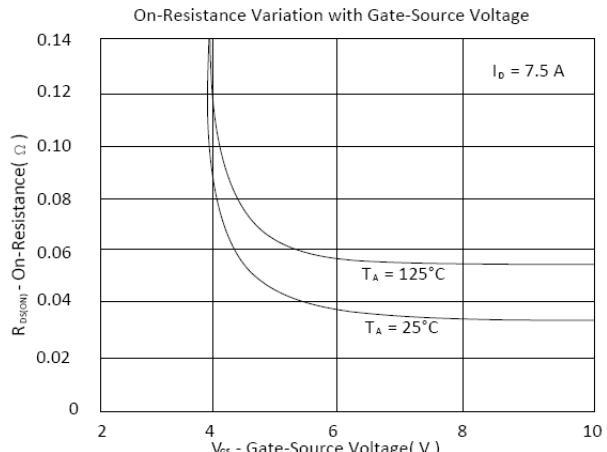
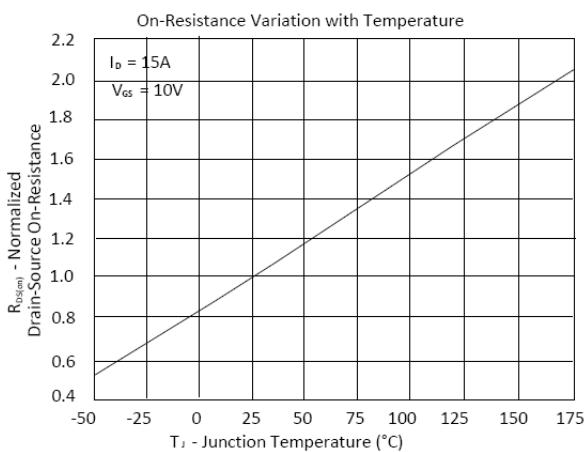
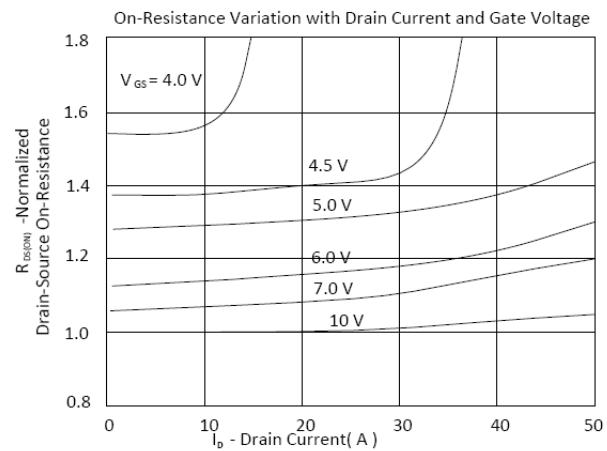
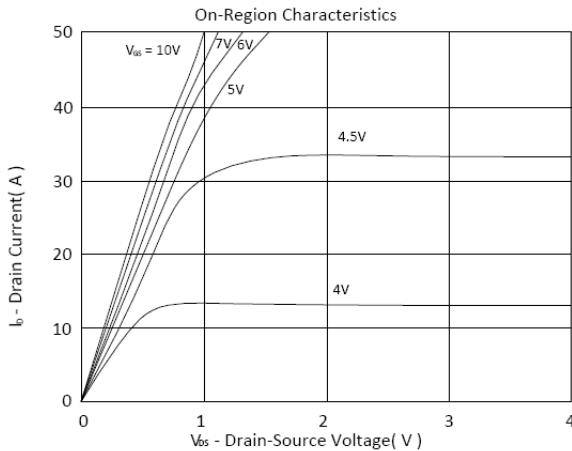
Note D: Independent of operating temperature.

Note E: Pulse width limited by maximum junction temperature.



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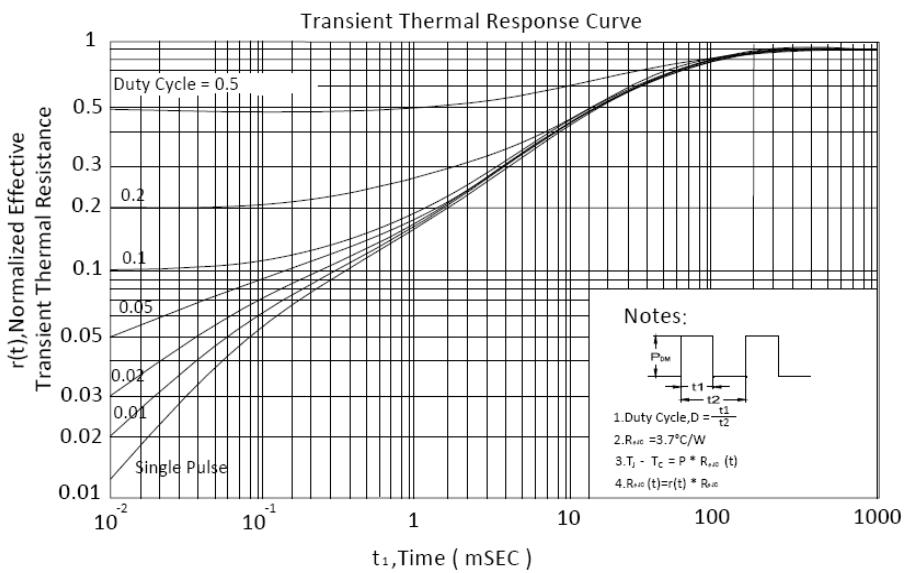
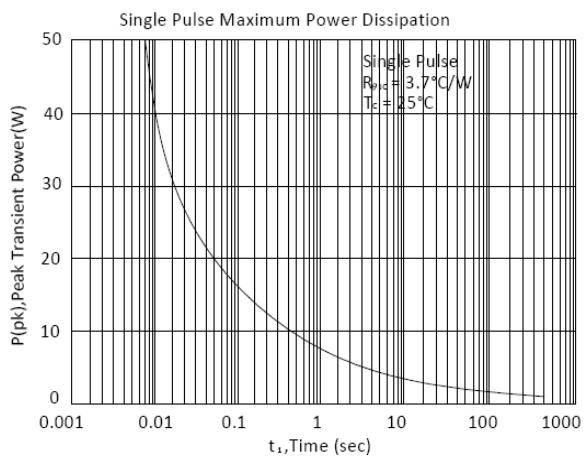
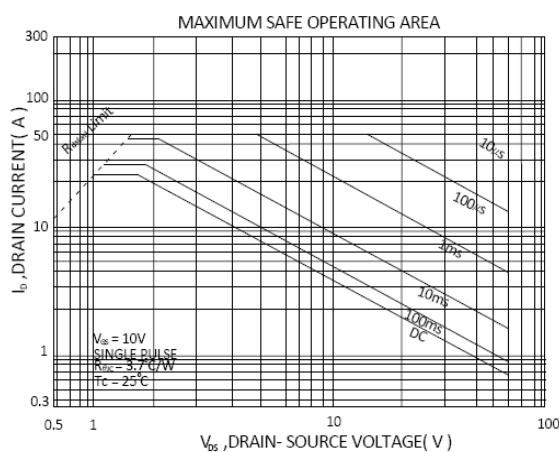
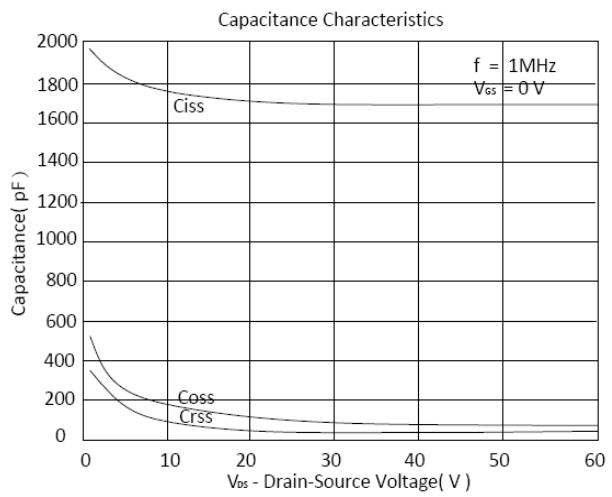
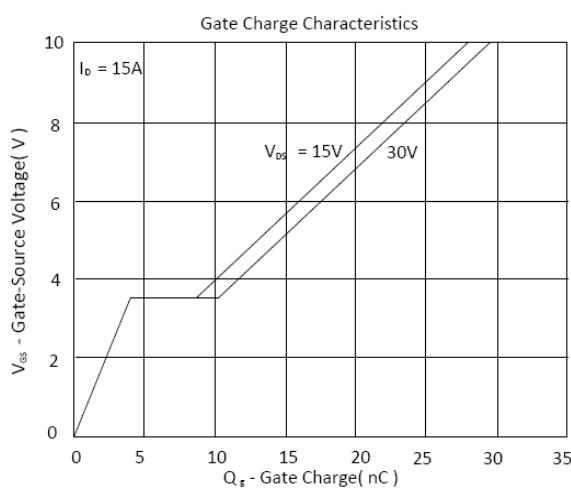
## ◆ TYPICAL CHARACTERISTICS (25°C Unless Noted)





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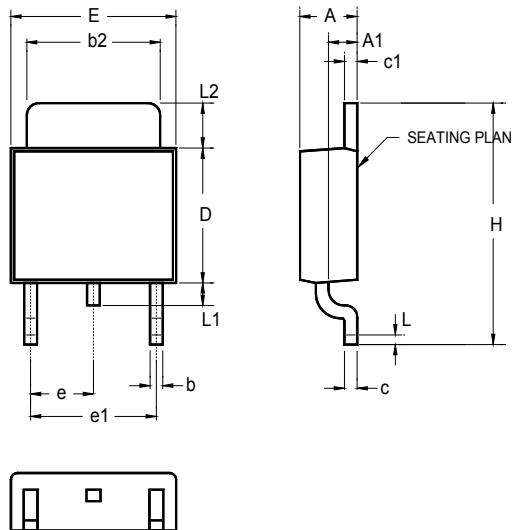




## N- Channel Enhancement Mode MOSFET

## ◆ PHYSICAL DIMENSIONS

## 3-Pin Surface Mount TO-252 (B)



	INCHES			MILLIMETERS		
	MIN	TYP	MAX	MIN	TYP	MAX
A	0.086	-	0.094	2.18	-	2.39
A1	0.040	-	0.050	1.02	-	1.27
b	-	0.024	-	-	0.61	-
b2	0.205	-	0.215	5.21	-	5.46
c	0.018	-	0.023	0.46	-	0.58
c1	0.018	-	0.023	0.46	-	0.58
D	0.210	-	0.220	5.33	-	5.59
E	0.250	-	0.265	6.35	-	6.73
e	0.090 BSC			2.29 BSC		
e1	0.180 BSC			4.58 BSC		
H	0.370	-	0.410	9.40	-	10.41
L	0.020	-	-	0.51	-	-
L1	0.025	-	0.040	0.64	-	1.02
L2	0.060	-	0.080	1.52	-	2.03