

P-Channel Enhancement Mode MOSFET
◆ DESCRIPTION

The MT2341 is the P-Channel logic enhancement mode power field effect transistor which is produced using high cell density, DMOS trench technology.

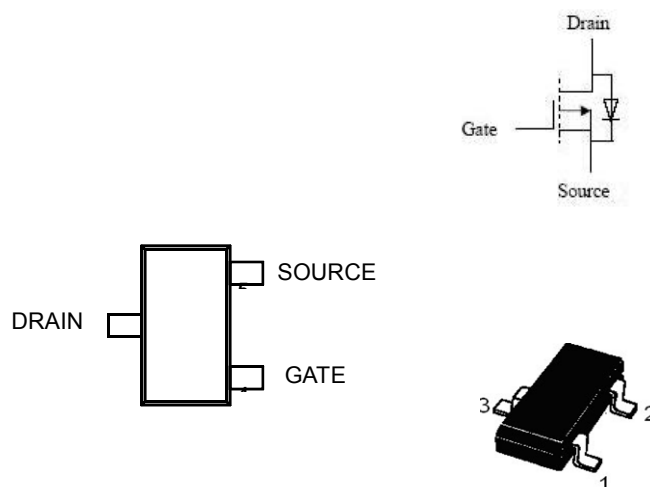
This high-density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management , other battery powered circuits, and low in-line power loss are required. The product is in a very small outline surface mount package.

◆ FEATURES

- -20V/-3.5A, $R_{DS(ON)} = 36 \text{ m}\Omega @ V_{GS} = 4.5\text{V}$
- -20V/-2.8A, $R_{DS(ON)} = 45 \text{ m}\Omega @ V_{GS} = 2.5\text{V}$
- -20V/-2.3A, $R_{DS(ON)} = 55 \text{ m}\Omega @ V_{GS} = 1.8\text{V}$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-23-3L package design

◆ APPLICATIONS

- POWER Management in Note
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC

◆ PIN CONFIGURATION


P-Channel Enhancement Mode MOSFET
◆ ABSOLUTE MAXIMUM RATINGS

 (T_A=25°C Unless Otherwise Noted)

Parameter		Symbol	Maximum	Unit
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	± 12	V
Continuous Drain Current	T _A = 25°C	I _D	-3.5	A
	T _A = 70°C		-2.8	
Pulsed Drain Current		I _{DM}	-12	A
Continuous Source Current (Diode Conduction)		I _S	-1.0	A
Power Dissipation	T _A = 25°C	P _D	1.25	W
	T _A = 70°C		0.8	
Operating junction temperature range		T _J	150	°C
Storage temperature range		T _{STG}	- 55 to 150	°C

◆ THERMAL RESISTANCE RATINGS

Parameter	Symbol	Maximum	Unit
Junction-to-Ambient	R _{θJA}	140	°C/W

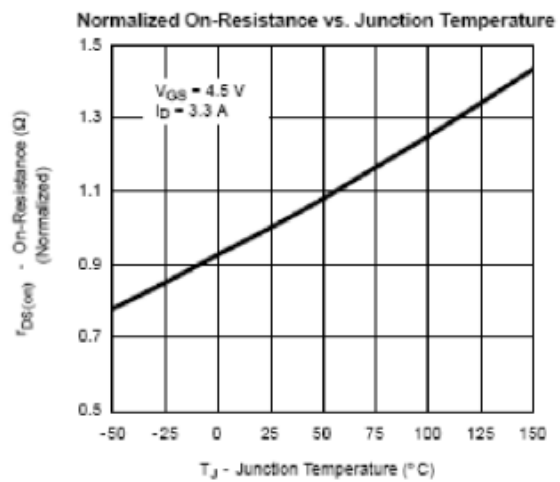
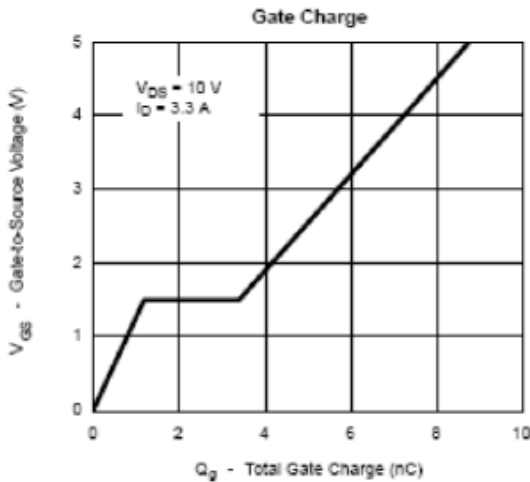
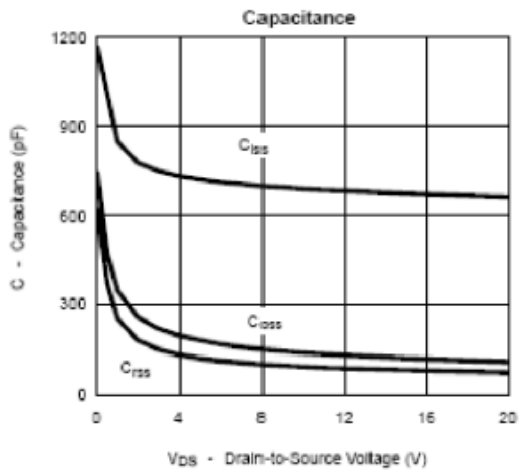
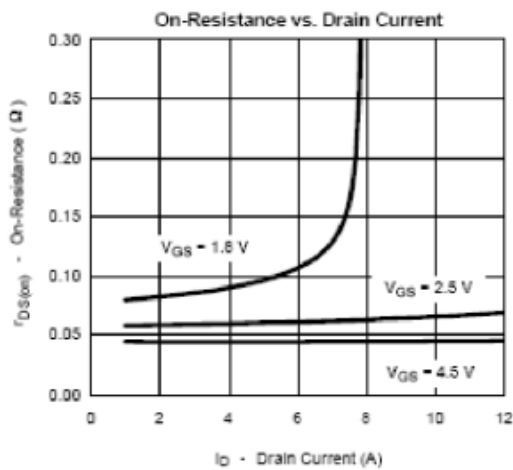
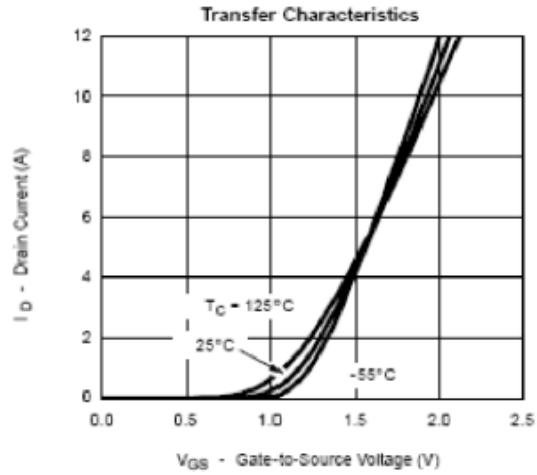
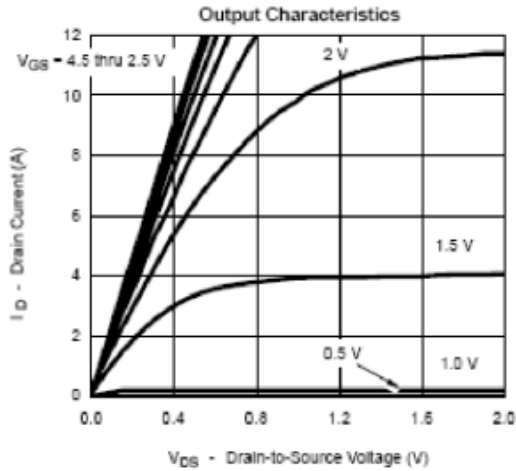
P-Channel Enhancement Mode MOSFET
◆ ELECTRICAL CHARACTERISTICS

 (T_A=25°C Unless Otherwise Noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D = -250μA	-0.35	-	-0.9	V
Gate Leakage Current	I _{GSS}	V _{DS} = 0V, V _{GS} = ± 20 V	-	-	±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0 V	-	-	-1	μA
		V _{DS} = -20V, V _{GS} = 0V, T _J = 55 °C	-	-	-10	
Forward Trans conductance	g _{fs}	V _{DS} = -5V, I _D = -3.5A	-	3.0	-	S
On-State Drain Current	I _{D(ON)}	V _{DS} ≤ -5V, V _{GS} = -4.5V	-6	-	-	A
Drain-Source On Resistance	R _{DS(ON)}	V _{GS} = -4.5V, I _D = -3.3A	-	36	-	mΩ
		V _{GS} = -2.5V, I _D = -2.8A	-	45	-	
		V _{GS} = -1.8V, I _D = -2.3A	-	55	-	
Diode Forward Voltage	V _{SD}	I _S = -1.0A, V _{GS} = 0V	-	-0.8	-1.2	V
Dynamic Parameters						
Input Cap.	C _{iss}	V _{DS} = -6V, V _{GS} = 0V, F = 1MHz	-	700	-	pF
Output Cap.	C _{oss}		-	160	-	
Reverse Transfer Cap.	C _{rss}		-	120	-	
Total Gate Charge	Q _g	V _{DS} = -6V, V _{GS} = -4.5V, I _D = -3.5A	-	8	13	nC
Gate-Source Charge	Q _{gs}		-	1.2	-	
Gate-Drain Charge	Q _{gd}		-	2.2	-	
Turn-On Time	T _{D(ON)}	V _{DS} = -6V, R _L = 6Ω, I _D = -1A, V _{GEN} = -4.5V, R _G = 6Ω	-	15	25	nS
	t _r		-	35	55	
Turn-Off Time	T _{D(OFF)}		-	60	90	
	T _f		-	30	40	

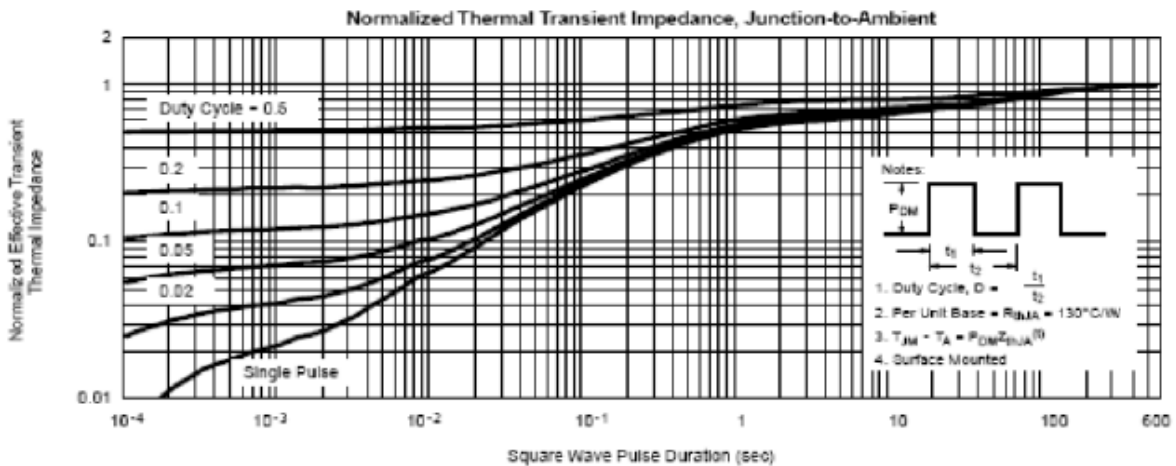
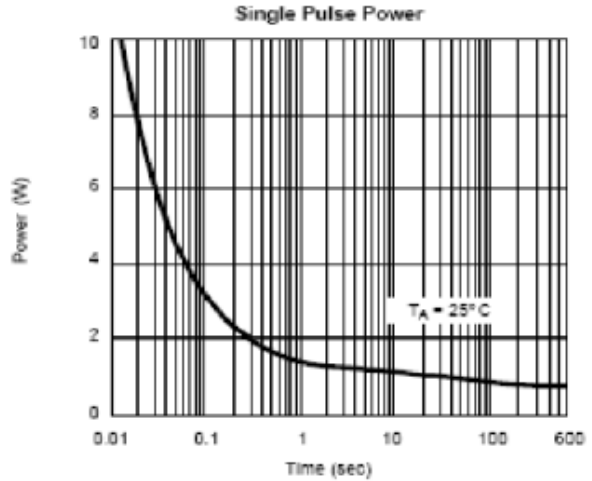
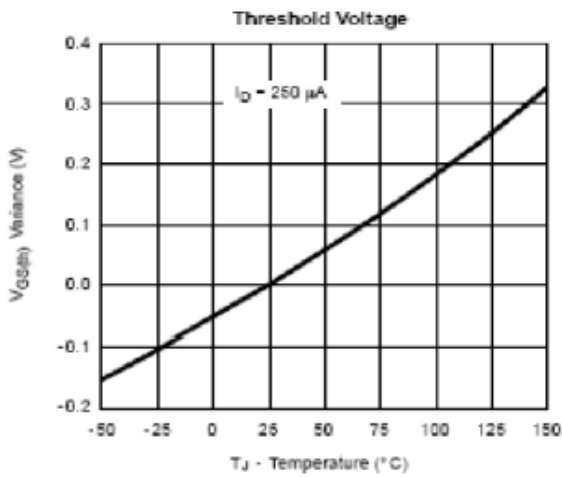
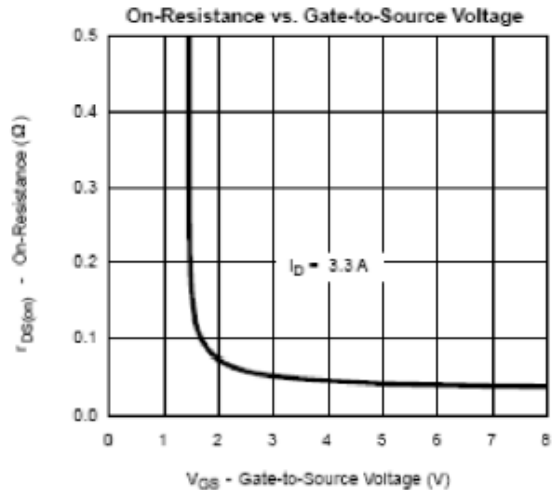
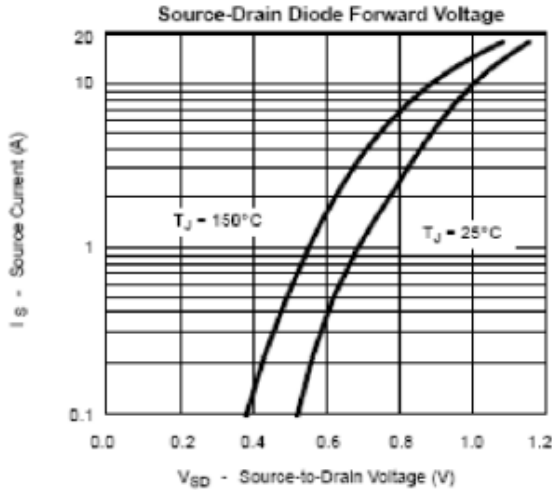
P-Channel Enhancement Mode MOSFET
◆ TYPICAL CHARACTERISTICS

(25°C Unless Noted)



P-Channel Enhancement Mode MOSFET
◆ TYPICAL CHARACTERISTICS

(25°C Unless Noted)



◆ PHYSICAL DIMENSIONS
3-Pin surface Mount SOT-23(S)
