

P- Channel Enhancement Mode MOSFET
◆ DESCRIPTION

The MT2303 is the P-Channel logic enhancement mode power field effect transistor are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

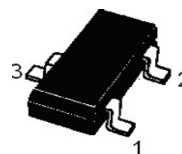
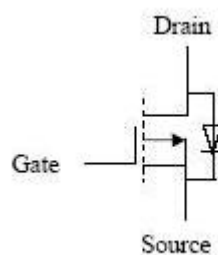
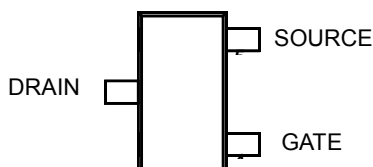
These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other Battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

◆ FEATURES

- -20V/-4.5A, $R_{DS(ON)} = 45m\Omega @ V_{GS} = -10V$
- -20V/-4.2A, $R_{DS(ON)} = 57m\Omega @ V_{GS} = -4.5V$
- -20V/-2.0A, $R_{DS(ON)} = 80m\Omega @ V_{GS} = -2.5V$
- -20V/-1.0A, $R_{DS(ON)} = 150m\Omega @ V_{GS} = -1.8V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-23-3L package design

◆ APPLICATIONS

- POWER Management in Note
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC

◆ PIN CONFIGURATION


P- Channel Enhancement Mode MOSFET
◆ ABSOLUTE MAXIMUM RATINGS

 (T_A=25°C Unless Otherwise Noted)

Parameter	Symbol	Maximum	Unit
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	± 12	V
Continuous Drain Current	I _D	T _A = 25 °C	-4.5
		T _A = 70 °C	-3.7
Pulsed Drain Current	I _{DM}	-10	A
Power Dissipation	P _D	T _A = 25 °C	1.25
		T _A = 70 °C	0.8
Operating junction temperature range	T _J	150	°C
Storage temperature range	T _{STG}	- 55 to 150	°C

◆ THERMAL RESISTANCE RATINGS

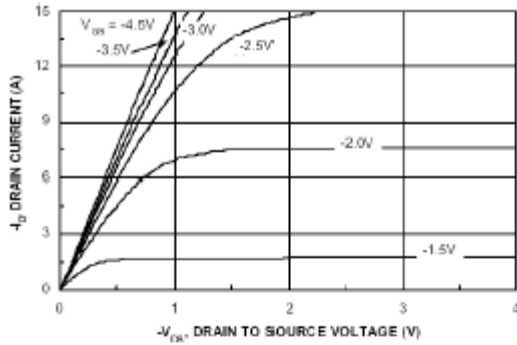
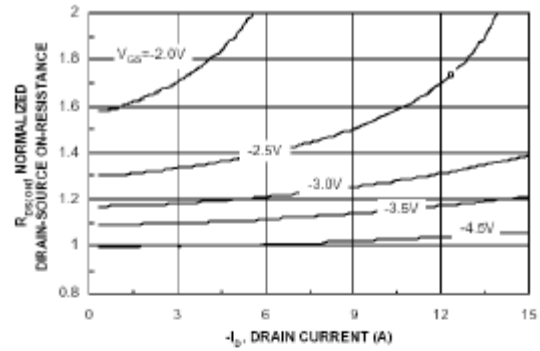
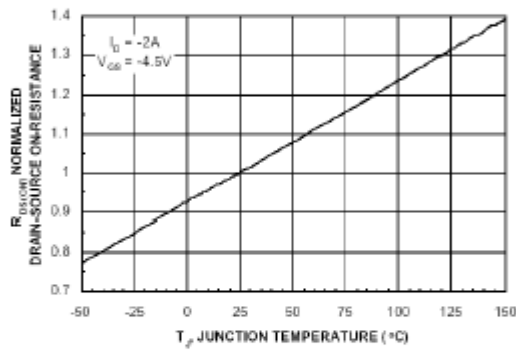
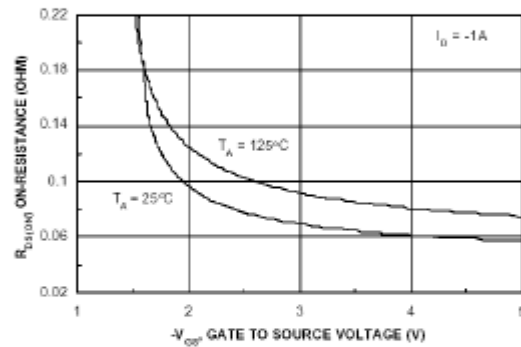
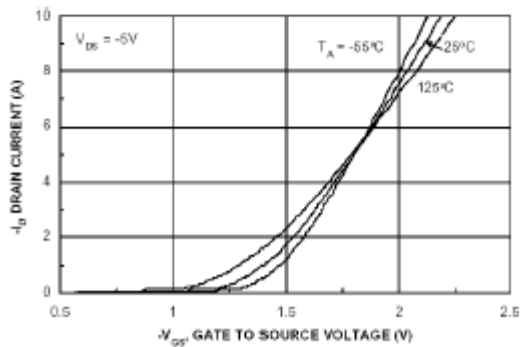
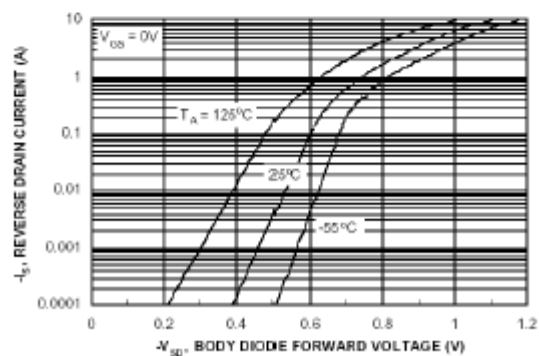
Thermal Resistance	Symbol	Maximum	Unit
Junction-to-Ambient	R _{θJA}	100	°C/W

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◆ ELECTRICAL CHARACTERISTICS

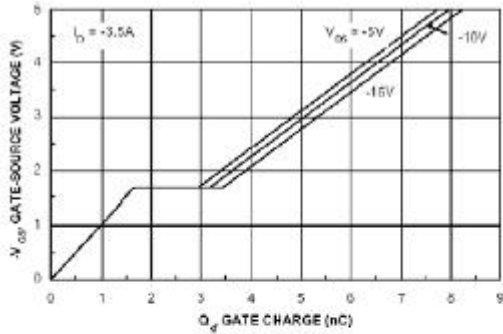
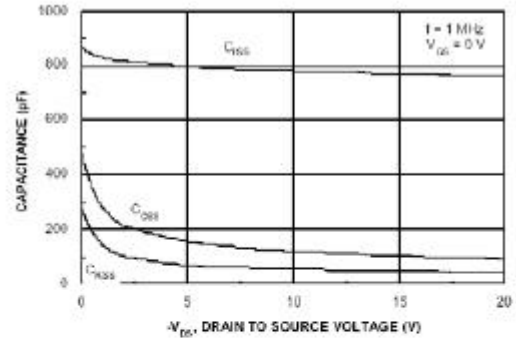
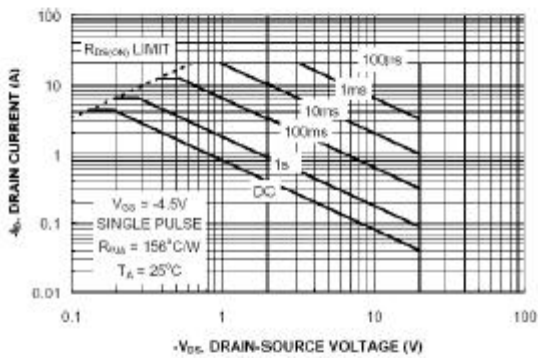
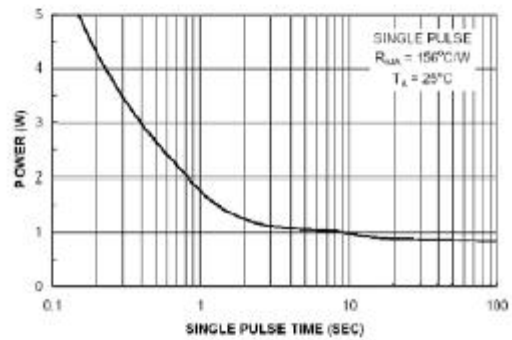
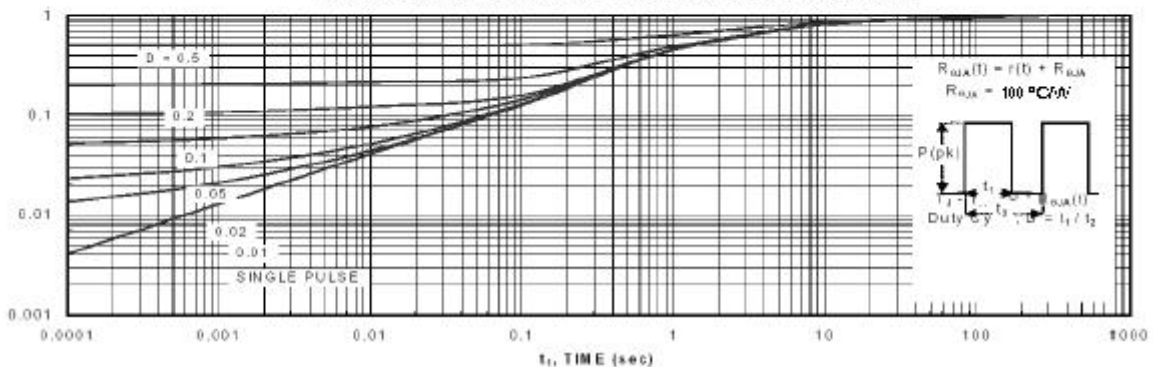
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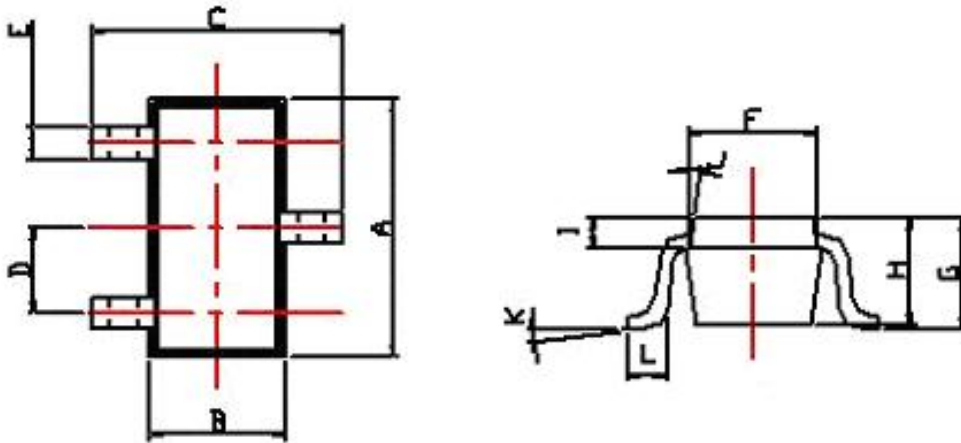
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -10μA	-20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D = -250μA	-0.6	-	-1.4	V
Gate Current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±12 V	-	-	±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0 V	-	-	-1	μA
		V _{DS} = -16V, V _{GS} = 0V, T _J = 55°C	-	-	-10	
Forward Transconductance	g _{fs}	V _{DS} = -5V, I _D = -2.8A	-	8	-	S
Drain-Source On Resistance	R _{DS(ON)}	V _{GS} = -10V, I _D = -4.5A	-	-	45	mΩ
		V _{GS} = -4.5V, I _D = -4.2A	-	-	57	
		V _{GS} = -2.5V, I _D = -2.0A	-	-	80	
		V _{GS} = -1.8V, I _D = -1.0A	-	-	150	
Diode Forward Voltage	V _{SD}	I _S = -1.25A, V _{GS} = 0V	-	-0.8	-1.2	V
Dynamic Parameters						
Input Cap.	C _{iss}	V _{DS} = -15V, V _{GS} = 0V, F = 1MHz	-	680	-	pF
Output Cap.	C _{oss}		-	107	-	
Reverse Transfer Cap.	C _{rss}		-	86	-	
Total Gate Charge	Q _g	V _{DS} = -15V, V _{GS} = -5V, I _D = -4.2A	-	6	-	nC
Gate-Source Charge	Q _{gs}		-	1.4	-	
Gate-Drain Charge	Q _{gd}		-	2.1	-	
Turn-On Time	t _{d(ON)}	V _{DS} = -15V, R _L = 3.6Ω, I _D = -4.2A, V _{GEN} = -10V, R _G = 6Ω	-	5.2	-	nS
	t _r		-	3.4	-	
Turn-Off Time	T _{d(OFF)}		-	32	-	
	T _f		-	2.5	-	

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◆ TYPICAL CHARACTERISTICS

 (T_A=25°C Unless Noted)

Figure 1. On-Region Characteristics

Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

Figure 3. On-Resistance Variation with Temperature

Figure 4. On-Resistance Variation with Gate to Source Voltage

Figure 5. Transfer Characteristics

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

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◆ TYPICAL CHARACTERISTICS

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Figure 7. Gate Charge Characteristic

Figure 8. Capacitance Characteristic

Figure 9. Maximum Safe Operating Area

Figure 10. Single Pulse Maximum Power Dissipation
Normalized Thermal Transient Junction to Ambient


◆ PHYSICAL DIMENSIONS
3-Pin surface Mount SOT-23


REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	0.9	1.4
B	1.20	1.66	H	0.8	1.30
C	2.37	2.90	I	0.25	0.7
D	0.85	1.15	J	$7 \pm 2^\circ$	
E	$0.350 + 0.15/-0.05$		K	$0 \sim 10^\circ$	
F	1.07	1.53	L	0.2 (MIN)	