◆ DESCRIPTION

The MT2300 is the N-Channel logic enhancement mode power field effect transistor are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

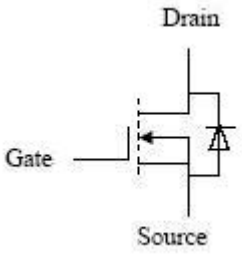
◆ FEATURES

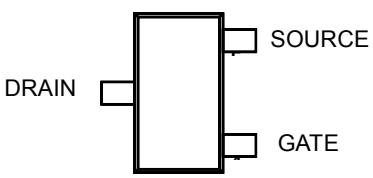
- 20V/6.0A, $R_{DS(ON)}$ = 28 mΩ @ V_{GS} = 10V
- 20V/5.0A, $R_{DS(ON)}$ = 36 mΩ @ V_{GS} = 4.5V
- 20V/4.5A, $R_{DS(ON)}$ = 40 mΩ @ V_{GS} = 2.5V
- 20V/4.0A, $R_{DS(ON)}$ = 48 mΩ @ V_{GS} = 1.8V
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- SOT-23-3L package design

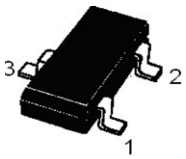
◆ APPLICATIONS

- POWER Management in Note
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

◆ PIN CONFIGURATION







◆ ABSOLUTE MAXIMUM RATINGS

(T_A=25℃ Unless Otherwise Noted)

Parameter		Symbol	Maximum	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	12	V
Continuous Drain Current	T_A = 25℃	I_D	6.0	A
	T_A = 70℃		4.0	
Pulsed Drain Current		I_{DM}	20	A
Continuous Source Current (Diode Conduction)		I_S	1.0	A
Power Dissipation	T_A = 25℃	P_D	1.25	W
	T_A = 70℃		0.8	
Operating junction temperature range		T_J	150	°C
Storage temperature range		T_{STG}	- 55 to 150	°C
Lead temperature(1/16” from case 10 sec)		T_{LEAD}	260	°C

◆ THERMAL RESISTANCE RATINGS

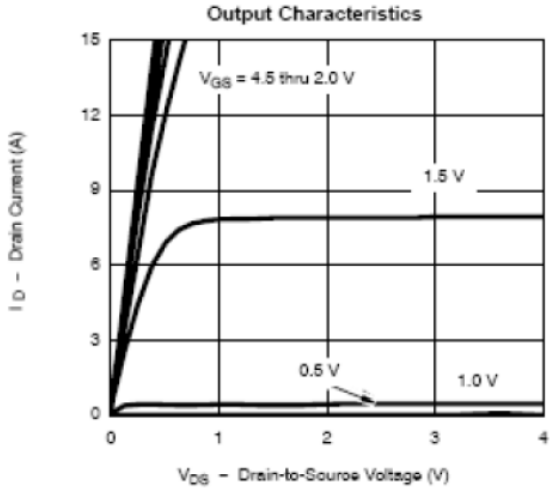
Parameter	Symbol	Maximum	Unit
Junction-to-Ambient	$R_{\theta JA}$	100	°C/W

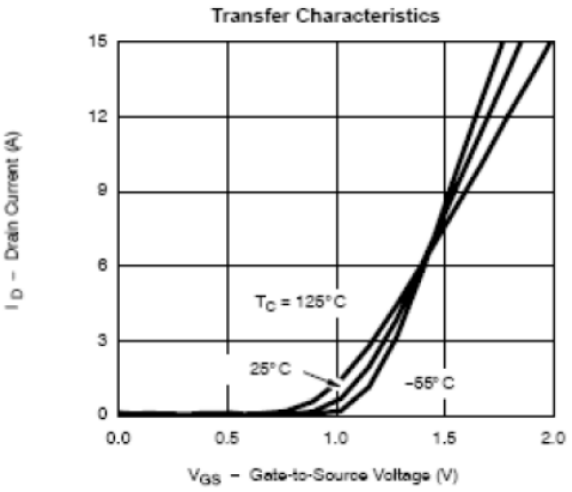
◆ ELECTRICAL CHARACTERISTICS

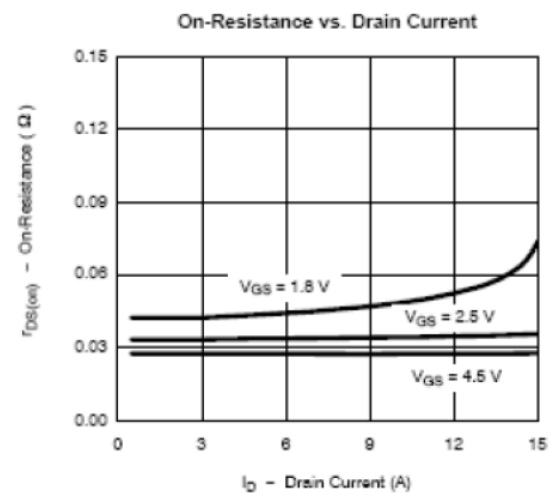
(T_A=25℃ Unless Otherwise Noted)

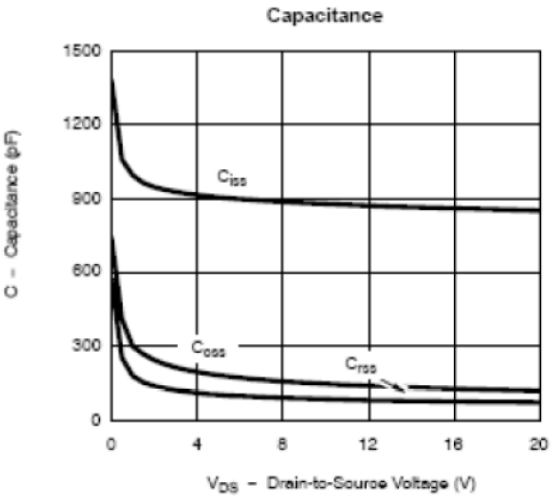
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS} = $V_{DS,}$ I_D = 250μA	0.4	-	1.0	V
Gate Leakage Current	I_{GSS}	V_{DS} = 0V, V_{GS} = ± 20 V	-	-	±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 20V, V_{GS} = 0 V	-	-	1	μA
		V_{DS} =20V, V_{GS} = 0V, T_J = 85 ℃	-	-	10	
Forward Trans conductance	g_{fs}	V_{DS} = 15V, I_D = 5.0A	-	30	-	S
On-State Drain Current	$I_{D(ON)}$	V_{DS} ≧ 5V, V_{GS} = 4.5V	6	-	-	A
Drain-Source On Resistance	$R_{DS(ON)}$	V_{GS} = 10V, I_D = 6.0A	-	22	28	mΩ
		V_{GS} = 4.5V, I_D = 5.0A	-	26	36	
		V_{GS} = 2.5V, I_D = 4.5A		29	40	
		V_{GS} = 1.8V, I_D = 4.0A		35	48	
Diode Forward Voltage	V_{SD}	I_S = 1.7A, V_{GS} = 0V	-	1.1	1.3	V
Dynamic Parameters						
Input Cap.	C_{iss}	V_{DS} = 10V, V_{GS} = 0V, F = 1MHz	-	600	-	pF
Output Cap.	C_{oss}		-	120	-	
Reverse Transfer Cap.	C_{rss}		-	100	-	
Total Gate Charge	Q_g	V_{DS} = 10V, V_{GS} = 4.5V, I_D = 6A	-	10	13	nC
Gate-Source Charge	Q_{gs}		-	1.4	-	
Gate-Drain Charge	Q_{gd}		-	2.1	-	
Turn-On Time	$T_{D(ON)}$	V_{DS} = 10V, R_L = 10Ω, I_D = 1A, V_{GEN} = 4.5V, R_G = 6Ω	-	15	25	nS
	t_r		-	40	60	
Turn-Off Time	$T_{D(OFF)}$		-	45	65	
	t_f		-	30	40	

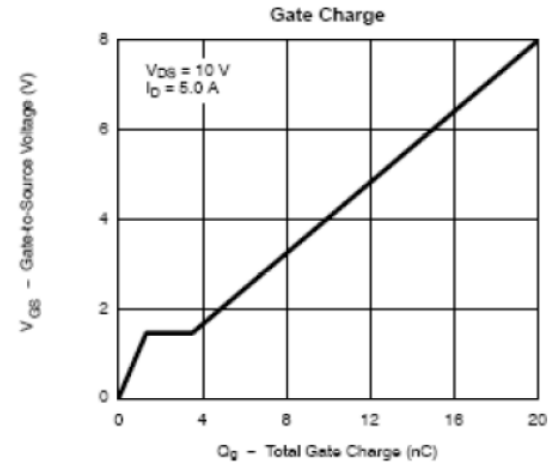
◆ TYPICAL CHARACTERICTICS

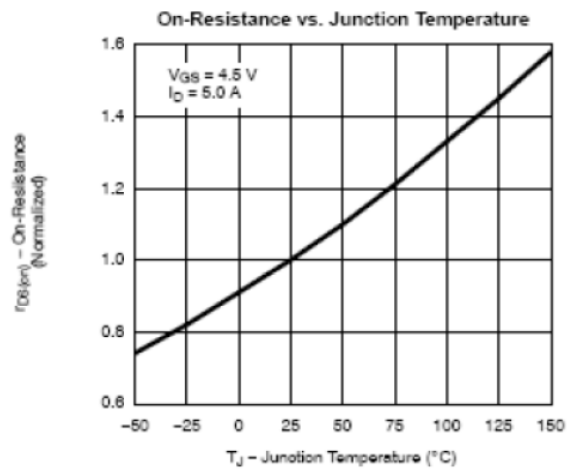




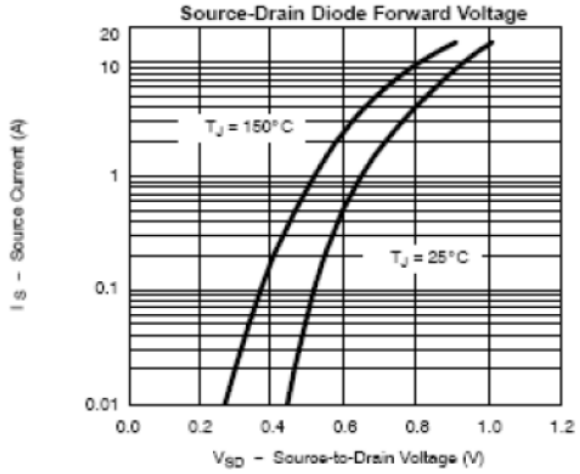


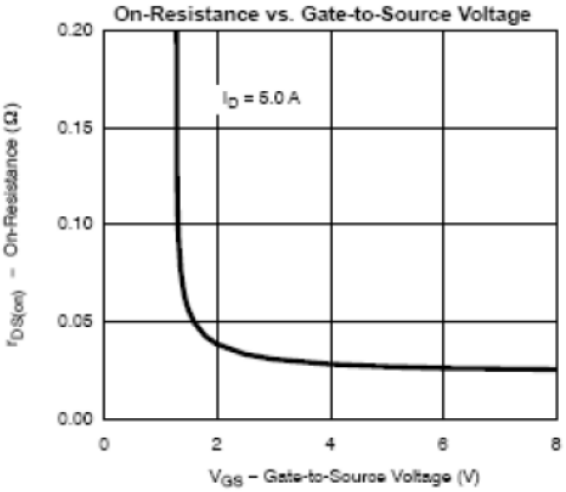


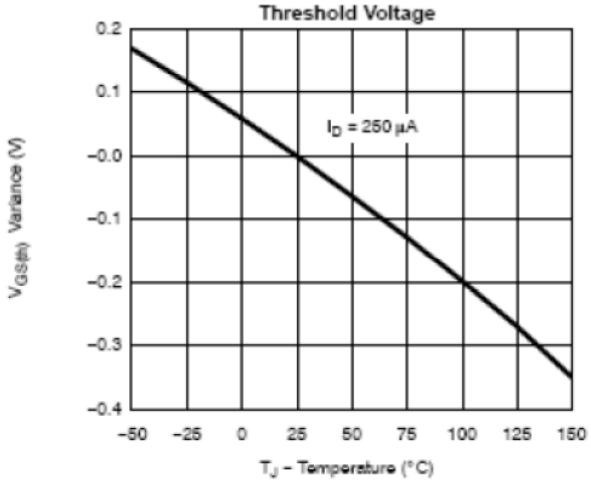


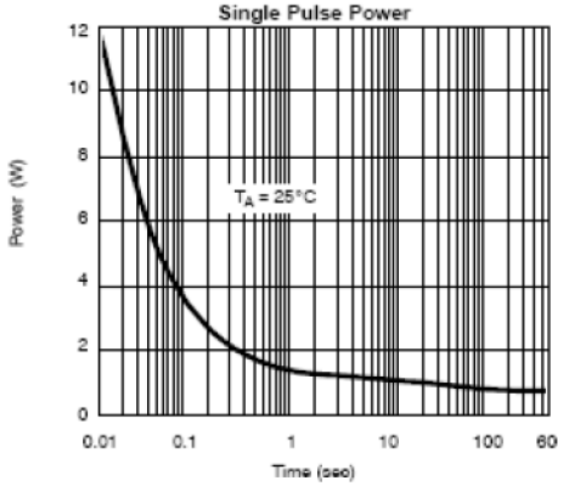


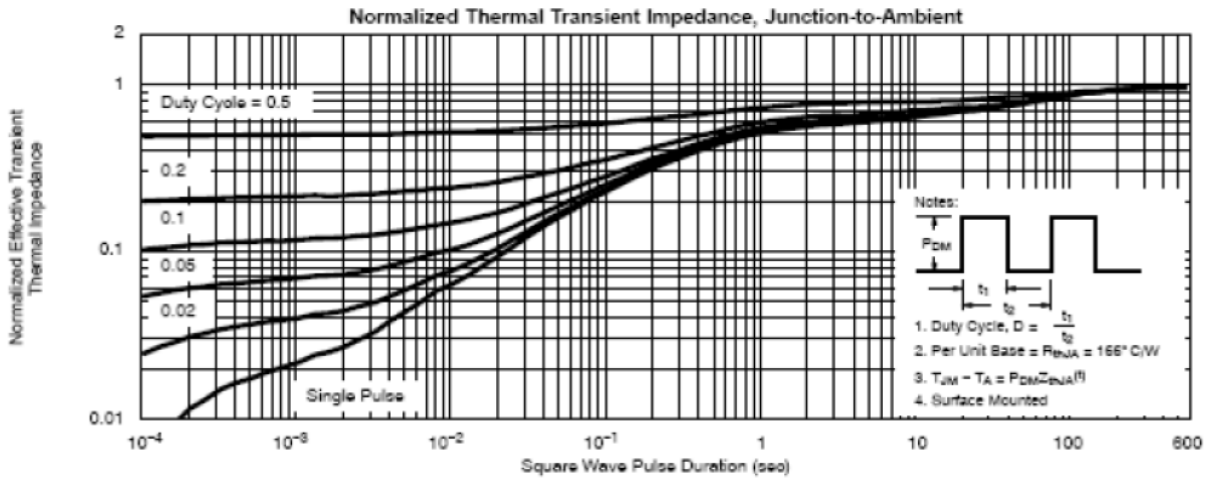
◆ TYPICAL CHARACTERICTICS





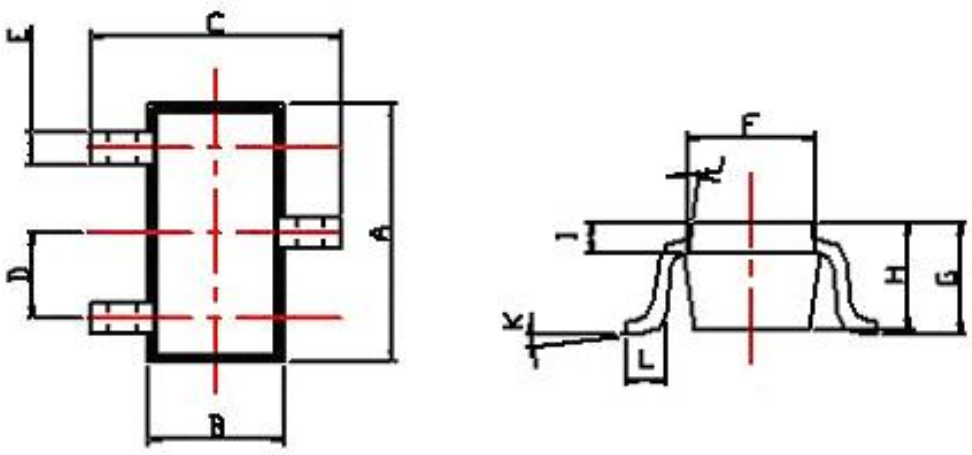






◆ PHYSICAL DIMENSIONS

3-Pin surface Mount SOT-23



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	0.9	1.4
B	1.20	1.66	H	0.8	1.30
C	2.37	2.90	I	0.25	0.7
D	0.85	1.15	J	7 ± 2°.	
E	0.350 + 0.15/-0.05		K	0 ~ 10°.	
F	1.07	1.53	L	0.2 (MIN)	